



Docket No.: M4065.0408/P408  
(PATENT)

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Terry R. Lee

Application No.: 09/712,173

Art Unit: 2115

Filed: November 15, 2000

Examiner: A. C. Wang

For: CLOCKING SYSTEM AND METHOD FOR  
HIGH SPEED DATA TRANSFER OVER A  
BUS

**AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)**

MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated August 23, 2004 (Paper No. 08092004), finally rejecting claims 1-5, 7-45 and 48-82, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 23 of this paper.

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AMENDMENTS TO THE CLAIMS

Claim 1 (currently amended): A method of providing clocking signals over a bus, said method comprising:

providing a first clock signal which travels over a first conductive path of said bus in a first direction;

providing a second clock signal which travels over a second conductive path of said bus in a second direction opposite to said first direction; and

causing said first and second clock signals to have a predetermined phase relationship with respect to each other at a predetermined location on said bus;

wherein

said predetermined location is at approximately a mid point of a plurality of spaced locations each adapted to accept a device, and supply said device with said first and second clock signals.

said predetermined phase relationship is substantially an in-phase relationship.

said first and second clock signals are independent signals,

one of said first and second clock signals is a data write clock signal, and

the other of said first and second clock signals is a data read clock signal.

Claim 2 (canceled):